2.1.5 XER Register (XER)

The fixed-point exception register (XER) is a 64-bit, user-level register and is described in *Figure 2-6* and *Table 2-6*.

Figure 2-6. XER Register
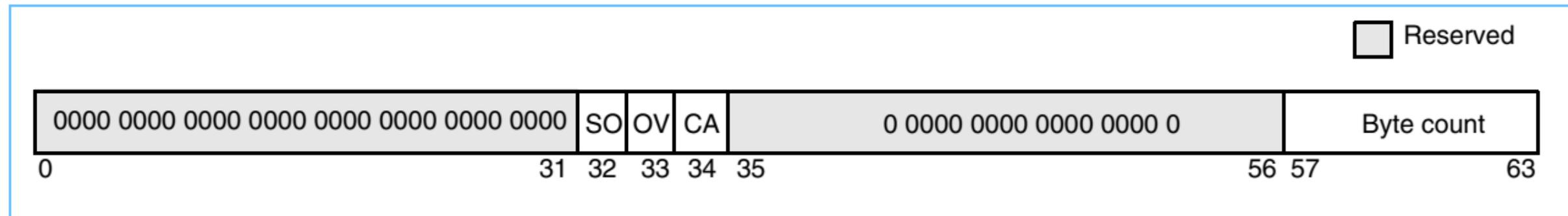
The bit definitions for XER, shown in *Table 2-6*, are based on the operation of an instruction considered as a whole, not on intermediate results. For example, the result of the Subtract from Carrying (**subfc***x*) instruction is specified as the sum of three values. This instruction sets bits in the XER based on the entire operation, not on an intermediate sum.